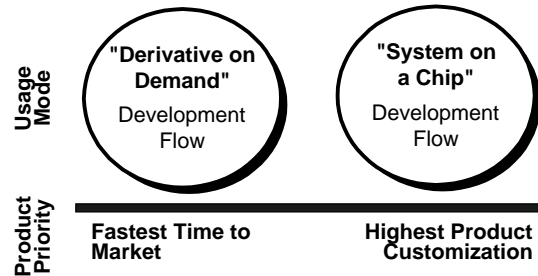


- **Powerful development environment for Triscend Configurable Processor (CPSU) devices**
- **Supports development flows that accommodate embedded system design needs**
  - “Derivative on Demand” flow maximizes time to market
  - “System on a Chip” flow maximizes product customization



**Figure 1. Development Flows Accommodate Embedded System Design Needs.**

### “Derivative on Demand” Flow

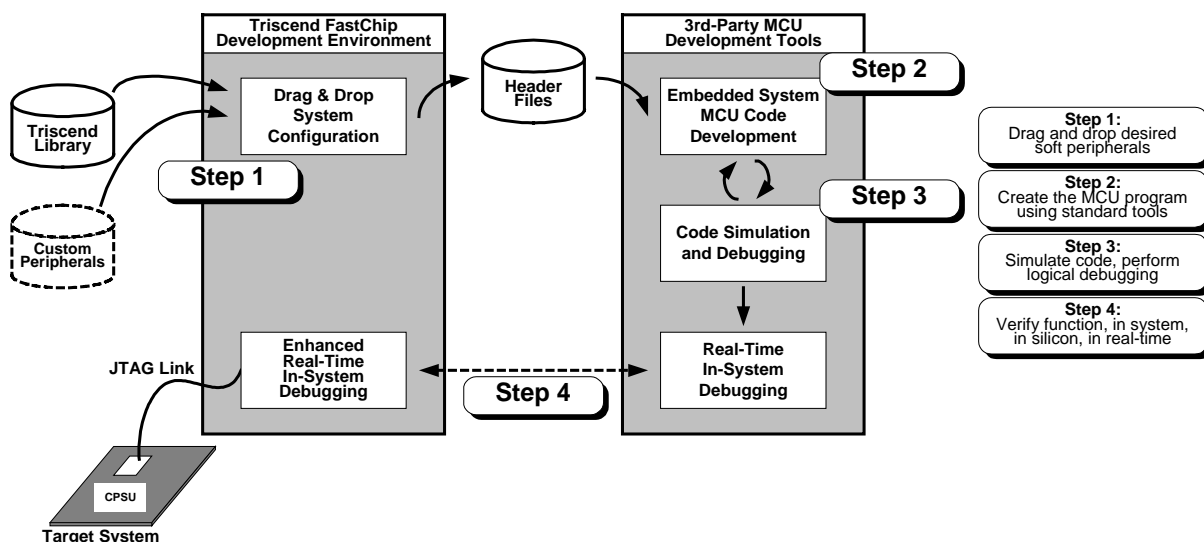
- **Fast, easy-to-learn development flow**
  - Highly graphical, Windows™-based
  - Preserves investment in existing processor development tools—reducing learning curve
- **Fast creation of a processor “derivative”**
  - Create a processor derivative in minutes
  - “Drag and Drop” soft peripheral modules into the CPSU’s configurable system logic
  - Includes a library of parameterized, pre-verified, pre-licensed, predictable “soft” processor peripherals
    - Timer/Counters
    - Programmable I/O
    - UART
    - Serial communication (SPI, I2C)
    - Pulse-width modulators (PWM)
    - FIFOs

### ■ **Fast MCU program development**

- Seamless interface to third-party processor software development tools
- Preserves existing software development flow for ‘C’ and assembly
- Leverages user’s existing code libraries
- Automatic header file generation simplifies software development

### ■ **Fast design debug and verification**

- Use industry standard tools for logical debug
- Fast JTAG connection allows industry-standard tools to control real-time, in-system debug
- Additional functions provide an extra level of system observability and control
  - Set hardware breakpoints
  - Examine and set CPSU internal values



**Figure 2. The Triscend “Derivative-on-Demand” Development Flow.**

Figure 3. Triscend FastChip software displays the CPSU embedded system, including the embedded processor, dedicated peripherals, and user-selected “soft” peripherals.

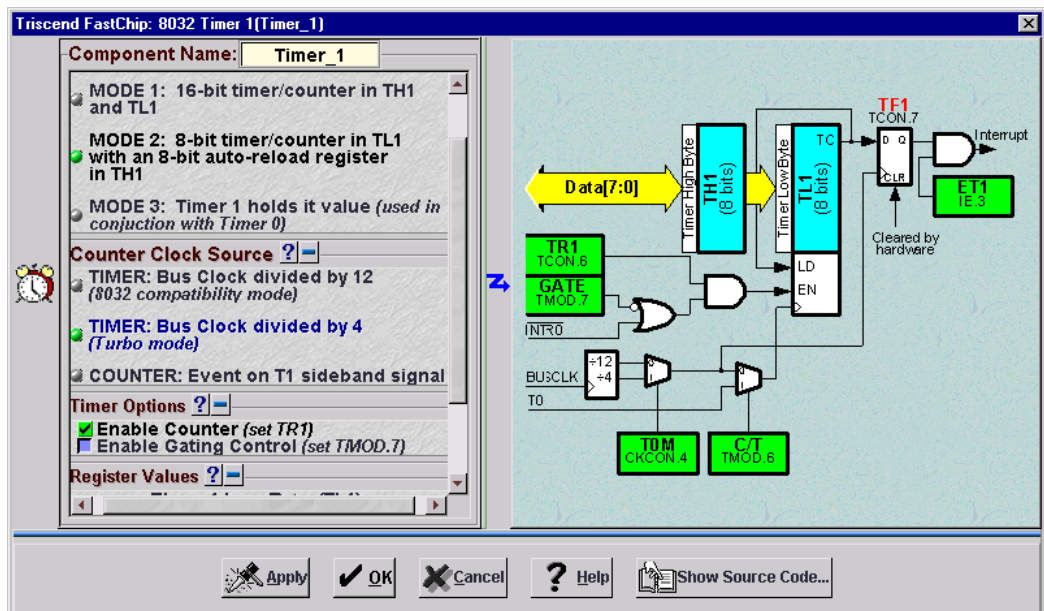
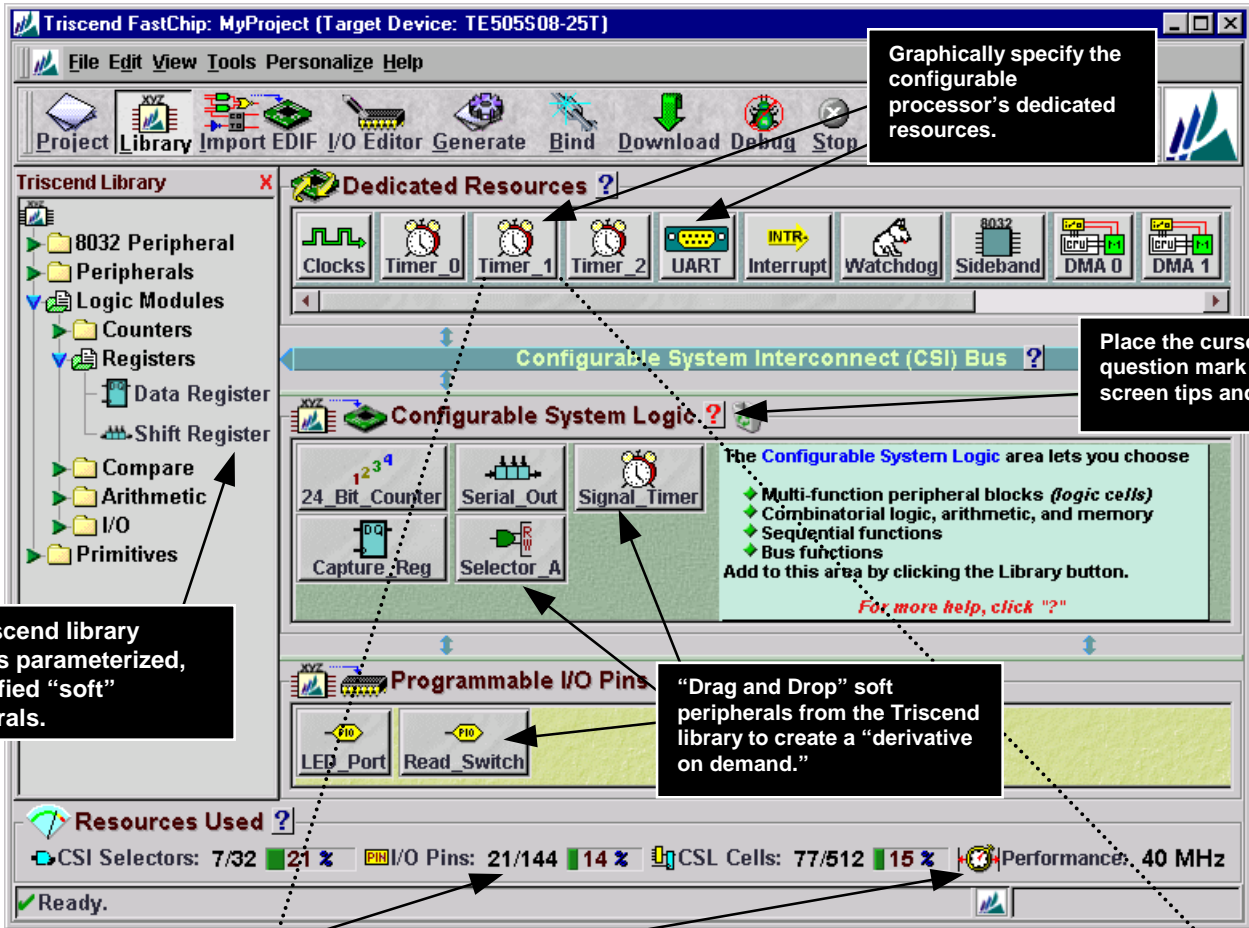


Figure 4. Parameterizing peripherals—both dedicated and "soft"—is fast and easy.

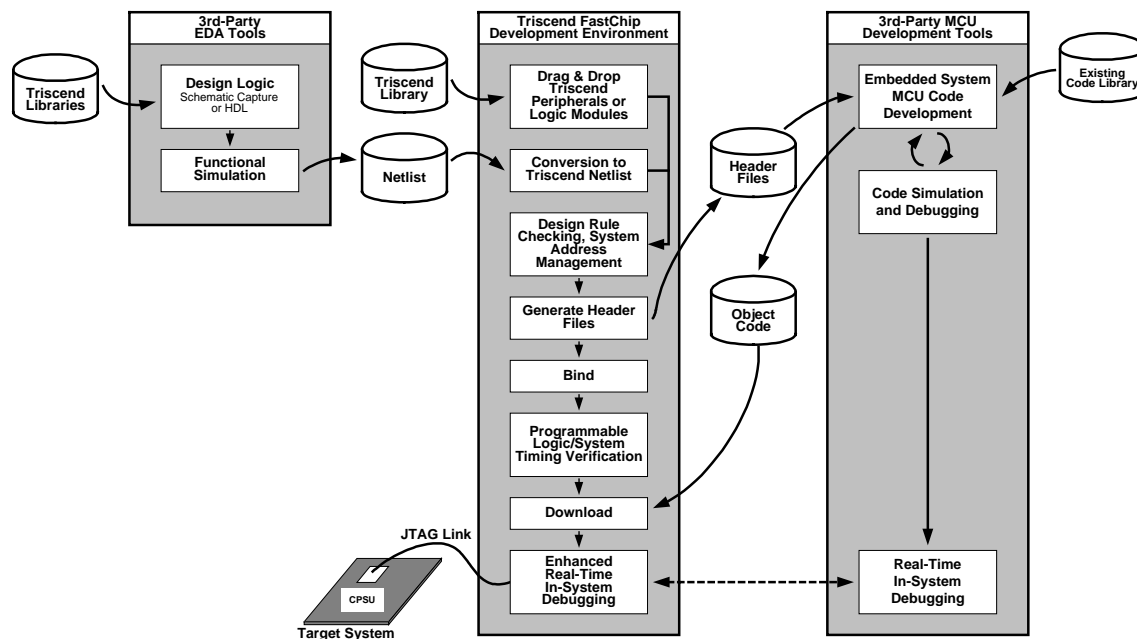


Figure 5. The Triscend “System on a Chip” Development Flow.

### “System on a Chip” Flow

■ “System on a chip” design flow maximizes product differentiation

- Custom logic development with industry-standard EDA tools
  - Schematic capture
  - Logic synthesis
- Supports ASIC-like design flow

### Easy to Use

■ Triscend FastChip software automates the design process

- Creates custom embedded systems using Triscend CPSU devices
- Creates a CPSU configuration image file Download directly to FLASH, EEPROM, or SRAM through JTAG
  - Save image in standard PROM formats

■ Operates on most PCs

- Pentium-class computer, 133 MHz or faster
- Windows '95, '98 or NT 4.0
- 48 to 64 Mbytes or more of RAM
- 800x600 (SVGA) graphics card (or better)
- Mouse
- Internet/Web connection recommended



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